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From a Finite State Machine to a Circuit

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Frank Vahid is the author of Digital Design with RTL Design, VHDL, and Verilog, 2nd Edition, published by Wiley.

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Digital Design with RTL Design, VHDL, and Verilog Second Edition by Frank Vahid University of California, Riverside John Wiley and Sons Publishers, 2011 <http://www.ddvahid.com> Book's site at Wiley The book's cover graphically depicts Moore's Law.

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Foreword (by Frank Vahid) > HDL (Hardware Description Language) based design has established itself as the modern approach to design of digital systems, with VHDL (VHSIC Hardware Description Language) and Verilog HDL being the two dominant HDLs. Numerous universities thus introduce their students to VHDL (or Verilog).

VHDL Tutorial: Learn by Example - Embedded System Design ...

Emphasis is placed on RTL design, where most modern digital design occurs. This material's HDL (Verilog and VHDL) coverage is intentionally template focused, teaching just enough of the HDLs to understand the templates. ... Frank Vahid Professor of Computer Science and Engineering, Univ. of California, Riverside.

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